## MAY 0 2 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Phadnis et al

Appl. No.: 09/976,004

Art Unit: 2661

Filed: October 15, 2001

Examiner: Robert W. Wilson

For

Reducing Overhead When Setting Up

Multiple Virtual Circuits Using

Signaling Protocols

Atty. Docket: CSCO-010/4390

## Supplemental Response to Final Office Action Under 37 C.F.R. § 1.116

Mail Stop <u>AF</u>
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

nt nter Sir:

In response to the Advisory Action dated April 20 2006 and the Final Office Action mailed December 13, 2005, Applicants submit the following remarks.

The listing of claims which begins on page 2 of this paper.

Remarks begin at page number 17 of this paper.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those which may otherwise be provided for indocuments accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to Deposit Account No.: 20-0674.